PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE: NEW ASSIGNMENT

NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date	
Geneticware Co., Ltd	12/08/2007	

RECEIVING PARTY DATA

Name:	Teklium Inc.		
Street Address:	1551 McCarthy Blvd. #203		
City:	Milpitas		
State/Country:	CALIFORNIA		
Postal Code:	ostal Code: 95035-7442		

PROPERTY NUMBERS Total: 4

Property Type	Number
Patent Number:	6138188
Patent Number:	6581127
Patent Number:	6779099
Patent Number:	6826689

CORRESPONDENCE DATA

Fax Number: (408)433-2358

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 4084332357
Email: jessie@teklium.com

Correspondent Name: Jessie Hsu

Address Line 1: 1551 McCarthy Blvd. #203

Address Line 4: Milpitas, CALIFORNIA 95035-7442

NAME OF SUBMITTER:

Jessie Hsu

Total Attachments: 2

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REEL: 022694 FRAME: 0548

OF \$160.00 6138188

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PATENT

ASSIGNMENT OF PATENTS

This Assignment of Patents ("Assignment") is made and entered into on this Law day of Dec. 2007, by and between:
Geneticware Co., Ltd., a(n) Br. Virgin Islands Corporation, with a principal place of business at P.O. Box 3444 Road Town, Tortola, Br. Virgin Island ("ASSIGNOR"); and Teklium Inc., a California Corporation, with a principal place of business at
1551 McCarthy Blvd., STE 203, Milpitas, CA 95035 ("ASSIGNEE").
WHEREAS, ASSIGNOR and ASSIGNEE have agreed to cause ASSIGNOR to assign and transfer to ASSIGNEE all of ASSIGNOR's respective right, title and interest in and to the patents identified herein.
NOW, THEREFORE , in consideration of the sum of ten dollars (US\$10.00), the mutual agreements of the parties set forth herein, and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the parties, intending to be legally bound, and upon the terms set forth herein, agree as follows:
ASSIGNOR does hereby irrevocably assign, sell, transfer and set over to ASSIGNEE, its successors and assigns, its entire right, title and interest in, to and under the patents and patent applications set forth on Attachment A attached hereto, including all rights pursuant to 35 U.S.C. § 154, any and all letters patents issuing from continuing, counterparts, divisional and continuation-in-part applications, substitutions, reissues, extensions, renewals and reexaminations thereof, any foreign counterparts thereof (collectively, the "Assigned Patents") throughout the world, including any and all causes of action and rights to damages and profits, due or accrued, relating to the foregoing, including the right to sue and recover for, and the right to profits and damages due or accrued arising out of or in connection with any and all past, present or future infringements or dilutions. The assignment of the Assigned Patents includes all documents related to the conception, diligence and reduction to practice of the inventions disclosed therein and all domestic and international patent filing documents.
IN WITNESS WHEREOF, ASSIGNOR has caused this Assignment of Patents Agreement to be executed by its duly authorized representative on the date set forth below:
ASSIGNOR Lien-In Lyn Name: Title:
On this day of d

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ATTACHMENT A

<u>Jurisdiction</u>	<u>Title</u>	Appl. Number	Filing Date	Issue No.	Issue Date
US	Buffer management device and method for improving buffer usage and access performance in data processing system	09/162,238	9.28.1998	6,138,188	10.24.2000
US	Operation method for controlling access attributes of a memorized page of a memory unit and its structure	09/908,645	7.20.2001	6,799,099	08.17.2004
US	Structure of controlled pipeline logic	09/924,708	8.07.2001	6,459,298	10.01.2002
us	Framework and method for inter- element channel transmission	09/477,286	1.04.2000	6,581,127	06.17.2003
US	IC chip security box	09/334,136	6.15.1999	6,245,992	06.12.2001
US	Method of IC packing/unpacking for preserving and updating data within the IC and the structure thereof	09/333,106	6.15.1999	6,519,841	02.18.2003
US	Security mechanism for an IC packing box	09/333,288	6.15.1999	6,049,275	04.11.2000
US	Method and system for emulating a secret code between two hardware modules	09/411,581	10.01.1999	6,826,689	11.30.2004

RECORDED: 05/18/2009

PATENT REEL: 022694 FRAME: 0550